

COURSE DETAIL

COMPUTER ARCHITECTURE

Country

New Zealand

Host Institution

University of Auckland

Program(s)

University of Auckland

UCEAP Course Level

Upper Division

UCEAP Subject Area(s)

Computer Science

UCEAP Course Number

134

UCEAP Course Suffix**UCEAP Official Title**

COMPUTER ARCHITECTURE

UCEAP Transcript Title

COMP ARCHITECTURE

UCEAP Quarter Units

6.00

UCEAP Semester Units

4.00

Course Description

This course examines modern processor architectures; principles of modern processor design; pipelining; memory hierarchies; I/O and network interfacing; compiler and OS support; embedded processors; performance; multiprocessing.

Language(s) of Instruction

English

Host Institution Course Number

COMPSYS 304

Host Institution Course Title

COMPUTER ARCHITECTURE

Host Institution Course Details

https://study.auckland.ac.nz/ords/r/uoa/catalogue/course?p6_code=COMPSYS%20304&...

Host Institution Campus

Auckland

Host Institution Faculty

Host Institution Degree

Host Institution Department

Computer Systems Engineering

Course Last Reviewed

2026-2027

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